

# megaSoC Datasheet

SoC Labs

December 6, 2024

Preamble, copyrights licenses etc.

# Contents

<b>1</b>	<b>Introduction</b>	<b>3</b>
1.1	Summary . . . . .	3
<b>2</b>	<b>System</b>	<b>4</b>
2.1	Bus Interconnect . . . . .	4
2.2	Address Map . . . . .	4
2.2.1	Summary . . . . .	4
2.2.2	Peripheral Region . . . . .	4
2.3	. . . . .	4
<b>3</b>	<b>Peripherals</b>	<b>5</b>
<b>4</b>	<b>Recommended Testboard</b>	<b>6</b>

# Chapter 1

## Introduction

### 1.1 Summary

# Chapter 2

## System

### 2.1 Bus Interconnect

### 2.2 Address Map

#### 2.2.1 Summary

Region	Start Address	End Address
Boot-Rom	0x00000000	0x0000FFFF
FLASH	0x00400000	0x007FFFFFFF
SRAM	0x00800000	0x0080FFFF
FLASH CTRL	0x01000000	0x0100FFFF
DMA CTRL	0x01010000	0x01011FFF
GIC	0x01100000	0x01107FFF
PERIPHERAL	0x40000000	0x5FFFFFFF
DEBUG	0x60000000	0x7FFFFFFF
DRAM	0x80000000	0xFFFFFFFF

#### 2.2.2 Peripheral Region

Below are the address regions for the System IO/Peripherals, detailed address maps for each peripheral are in chapter 3

Region	Start Address	End Address
UART 0	0x40000000	0x40000FFF
Timer 1	0x40001000	0x40001FFF

### 2.3

# Chapter 3

## Peripherals

## Chapter 4

# Recommended Testboard